

FIG.1 (PRIOR ART)

FIG. 2

66	52	64	54	50	56	58	60	62
	REG	EX1	EX2	EX3			CMT	
clk 1	ld r A<-[rC]						ld r A<-[rB]	
clk 2		ld r 10<-[rC] (t)						
clk 3			ld r A<-[rC] (t)					
.
clk n								

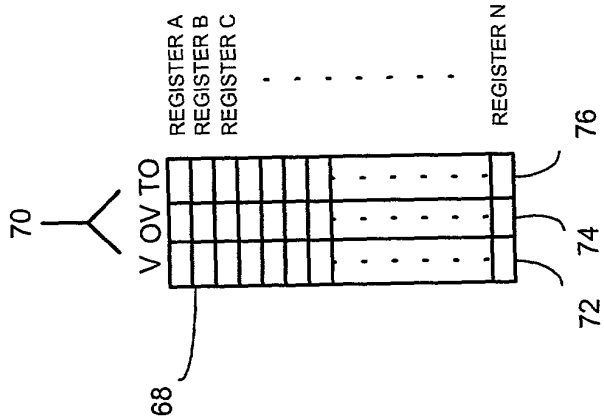


FIG. 2

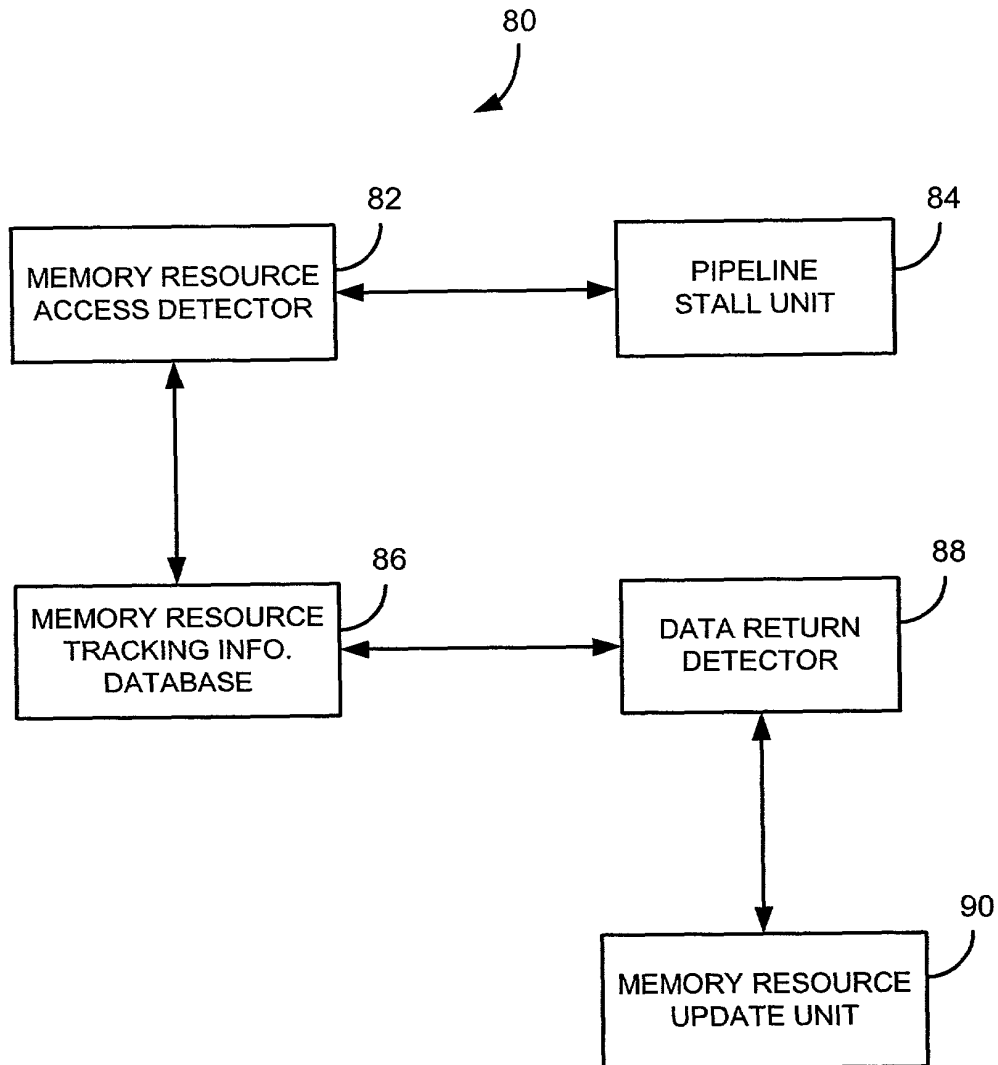


FIG. 3

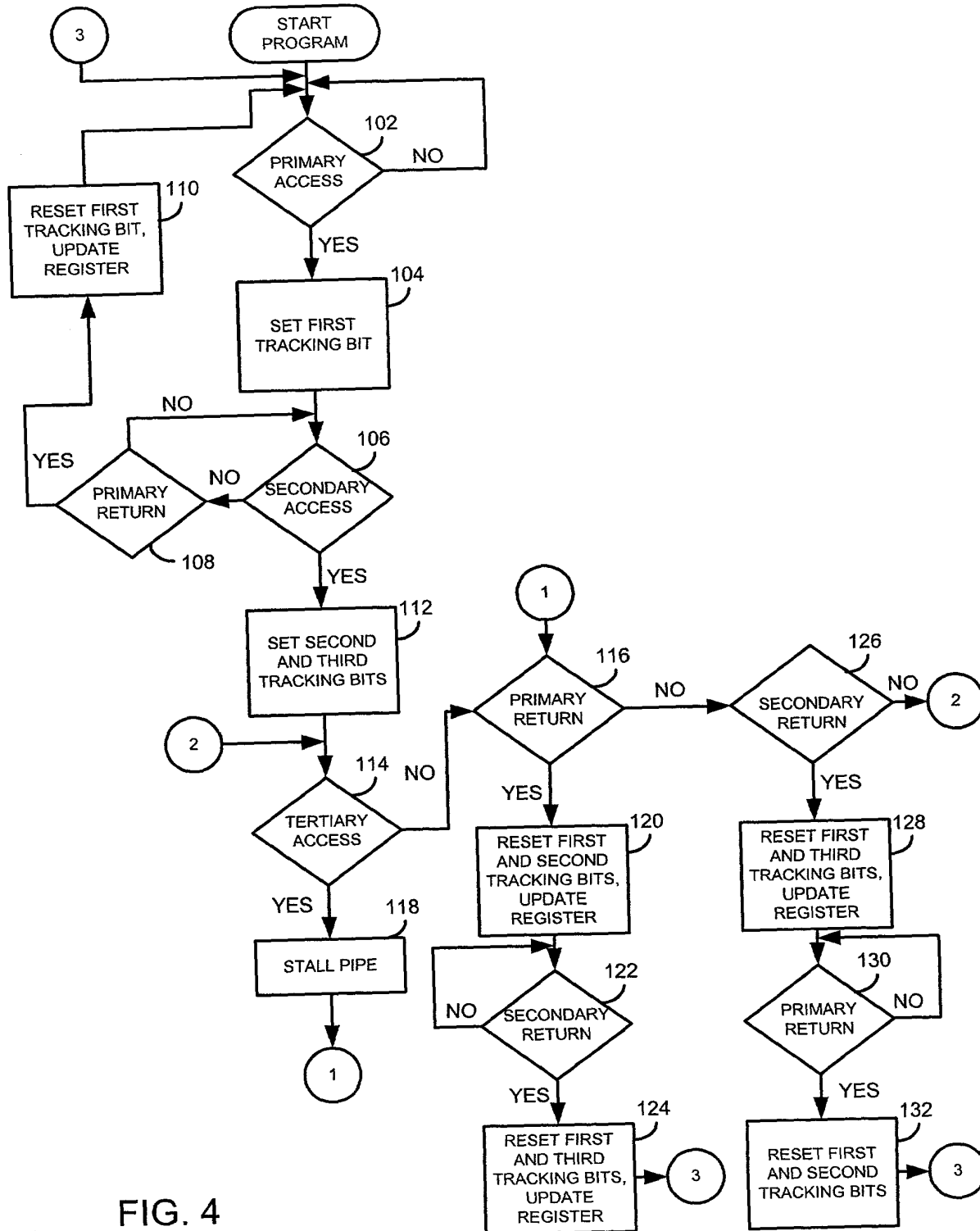


FIG. 4

150



V	OV	TO	
0	0	0	NO PENDENCY
0	0	1	IN ORDER DATA RETURNS
0	1	0	OUT OF ORDER DATA RETURNS (WAW HAZARD)
0	1	1	UNUSED STATE
1	0	0	PRIMARY PENDENCY
1	0	1	UNUSED STATE
1	1	0	UNUSED STATE
1	1	1	PRIMARY AND SECONDARY PENDENCY

FIG. 5

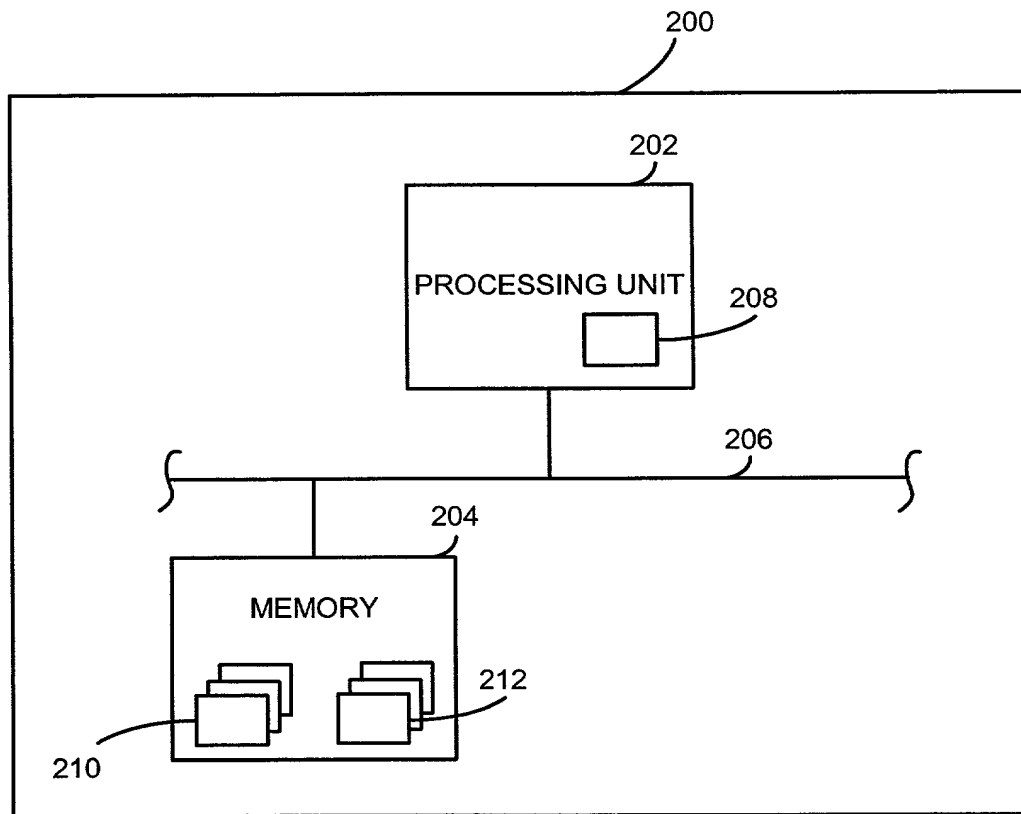


FIG. 6